

DOCKET NO. TI 37691

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Husam N. Alshareef, et al.

Serial No.: N/A

Filed: Herewith

For: A SEMICONDUCTOR DEVICE HAVING MULTIPLE WORK  
FUNCTIONS AND METHOD OF MANUFACTURE THEREFOR

Group No.: N/A

Examiner: N/A

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

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INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement.

This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO-1449. Copies of the listed references are submitted herewith.

U.S. Patent No.

Inventor

Date

6,660,577 B2

Chen et al.

December 9, 2003

References:

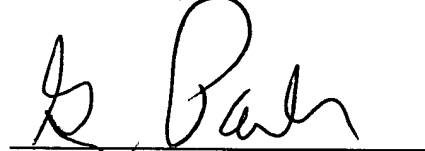
Qiang Lu, et al.; "METAL GATE WORK FUNCTION ADJUSTMENT FOR FUTURE CMOS TECHNOLOGY"; 2001 Symposium on VLSI Technology Digest of Technical Papers; 2001 IEEE.

Pushkar Ranade, et al.; "TUNABLE WORK FUNCTION MOLYBDENUM GATE TECHNOLOGY FOR FDSOI-CMOS"; IEDM, 2002 IEEE; Pgs 363-366.

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Respectfully submitted,

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Substitute for form 1449/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

**(Use as many sheets as necessary)**

**Complete if Known**

Application Number	TI 37961
Filing Date	Herewith
First Named Inventor	Husam N. Alshareef
Art Unit	N/A
Examiner Name	N/A
Attorney Docket Number	TI 37691

Sheet	1	of	2
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## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	TI 37961
				Filing Date	Herewith
				First Named Inventor	Husam N. Alshareef
				Art Unit	N/A
				Examiner Name	N/A
Sheet	2	of	2	Attorney Docket Number	TI 37691

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Qiang Lu, et al.; "METAL GATE WORK FUNCTION ADJUSTMENT FOR FUTURE CMOS TECHNOLOGY"; 2001 Symposium on VLSI Technology Digest of Technical Papers; 2001 IEEE.	
		Pushkar Ranade, et al.; "TUNABLE WORK FUNCTION MOLYBDENUM GATE TECHNOLOGY FOR FDSOI-CMOS"; IEDM, 2002 IEEE; Pgs 363-366.	

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